























The Pile (2020)

2.9 USPTO Backgrounds

USPTO Backgrounds is a dataset of background sections from patents granted by the United States Patent and Trademark Office, derived from its published bulk archives⁵. A typical patent background lays out the general context of the invention, gives an overview of the technical field, and sets up the framing of the problem space. We included USPTO Backgrounds because it contains a large volume of technical writing on applied subjects, aimed at a non-technical audience.

nductivity types), it is necessary that at least some process is steps differentiate between p-type and n-type transistors. Separate implant steps, for example, are needed to define n-well and p-well structures and to dope the source/drain regions of n-channel and p-channel transistors. Whenever possible, however, it is generally desirable to use a single process step to define transistor features regardless of the transistor type. Single process steps imply a single mask step, which is always desirable to

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#	Team	Members		Score	Entries	Last	Code
1			۵	0.848	9	5d	
2			۵	0.848	61	18h	
3			Ø .	0.847	81	10h	
4			()	0.847	22	1d	
5			۵	0.846	8	16d	
6			۵	0.846	29	2d	
7			0	0.846	46	3h	
8			0	0.845	24	14h	

